



PTO/SB/08A (10-01)

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Substitute for form 1449A/PTO				Complete if known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/076,013
				Filing Date	February 13, 2002
				First Named Inventor	Fallon
				Art Unit	2611
				Examiner Name	Tesfaldet Bocure
Sheet	1	of	3	Attorney Docket Number	8011-16

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/T.B./		5,434,983	07-18-1995	Yaso et al.	/
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		6,604,040	08-05-2003	Kawasaki et al.	/
↓		6,661,845	12-09-2003	Herath	/
/T.B./		6,717,534	04-06-2004	Yokose	/

Examiner Signature	/Tesfaldet Bocure/	Date Considered	08/18/2007
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					Attorney Docket Number	8011-16

/T.B./		6,731,814	05-04-2004	Zeck et al	✓
		6,756,922	06-29-2004	Ossia	✓
		6,810,434	10-26-2004	Muthujumaraswathy et al.	✓
		6,885,316	04-26-2005	Mehring	✓
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		6,909,383	06-21-2005	Shokrollahi et al.	✓
		7,102,544	09-05-2006	Liu	✓
		7,130,913	10-31-2006	Fallon	✓
		7,161,506	01-09-2007	Fallon	✓
		7,190,284	03-13-2007	Dye et al.	✓
✓		2001/0031092	10-18-2001	Zeck et al.	✓
/T.B./		2002/0101367	08-01-2002	Geiger et al.	✓

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
/T.B./		MILLMAN, HOWARD, "Image and video compression", Computerworld, Vol. 33, Issue No. 3, January 18, 1999, pp. 78	✓
/T.B./		"IBM boosts your memory", Geek.com [online], June 26, 2000 [retrieved on July 6, 2007], <URL: http://www.geek.com/ibm-boosts-your-memory/ >	✓
/T.B./		"IBM Research Breakthrough Doubles Computer Memory Capacity", IBM Press Release [online], June 26, 2000 [retrieved on July 6, 2007], <URL: http://www-03.ibm.com/press/us/en/pressrelease/1653.wss >	✓

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/T.B./		"ServerWorks To Deliver IBM's Memory eXpansion Technology in Next-Generation Core Logic for Servers", ServerWorks Press Release [online], June 27, 2000 [retrieved on July 14, 2000], <URL: http://www.serverworks.com/news/press/000627.html >	✓
/T.B./		ABALI, B., et al., "Memory Expansion Technology (MXT) Software support and performance", IBM Journal of Research and Development, Vol. 45, Issue No. 2, March 2001, pp. 287-301	✓
/T.B./		FRANASZEK, P. A., et al., "Algorithms and data structures for compressed-memory machines", IBM Journal of Research and Development, Vol. 45, Issue No. 2, March 2001, pp. 245-258	✓
/T.B./		FRANASZEK, P. A., et al., "On internal organization in compressed random-access memories", IBM Journal of Research and Development, Vol. 45, Issue No. 2, March 2001, pp. 259-270	✓
/T.B./		SMITH, T.B., et al., "Memory Expansion Technology (MXT) Competitive impact", IBM Journal of Research and Development, Vol. 45, Issue No. 2, March 2001, pp. 303-309	✓
/T.B./		TREMAINE, R. B., et al., "IBM Memory Expansion Technology (MXT)", IBM Journal of Research and Development, Vol. 45, Issue No. 2, March 2001, pp. 271-285	✓

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